

## **Software Independence**

1. An undetected error or fault in the voting system's software or hardware shall not be capable of causing an undetectable change in election results.
  - 1.1. A software error or fault within the on the vote capture device shall not lead to the reporting of an incorrect election outcome.
  - 1.2. Misconfigured software within the vote capture device shall not lead to the reporting of an incorrect election outcome.
  - 1.3. Malware or other malicious software on the vote capture device shall not lead to the reporting of an incorrect election outcome.
  - 1.4. Any privileged user on the voting system shall not have the ability to lead to the reporting of an incorrect election outcome.
  - 1.5. An error within a hardware component of the voting system shall not lead to the reporting of an incorrect election outcome.
  - 1.6. Intentionally compromised hardware shall not lead to the reporting of an incorrect election outcome.
2. The voting system SHALL produce tamper-evident records to enable detection of incorrect election outcomes.
  - 2.1. Tamper-evident records shall provide voters the opportunity to verify that their ballot selections were properly recorded by the voting system.
  - 2.2. Tamper-evident records shall be automatically created as a result of a voter inputting ballot selections into the voting system.
  - 2.3. Detected errors shall be recorded in a tamper-evident manner.
3. Voting systems records shall enable external auditors to verify that cast ballots were correctly tabulated.

- 3.1. The voting system shall enable a meaningful audit in the presence of compromised or malicious software resident on the system.
- 3.2. The voting system shall enable a meaningful audit in the presence of compromised hardware components.
4. Voting systems shall document and provide a method, either through procedural or technical means, for voters to report detected errors or incorrect results.

Additionally, voting systems shall meet the requirements within the Paper-based System Architecture *or* Fully-electronic System Architecture sections.

**Paper-based System Architectures**

**Fully-electronic System Architectures**